

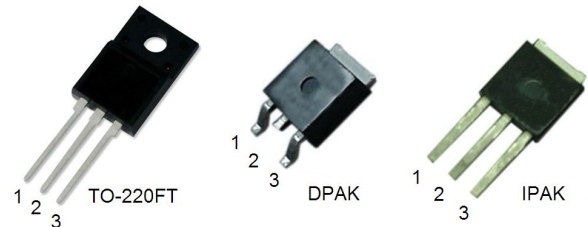


N 沟道增强型场效应晶体管 N-CHANNEL MOSFET

HW2N65

主要参数 MAIN CHARACTERISTICS

I_D	2A
V_{DSS}	650V
$R_{DS(ON)}$ ($V_{GS}=10V$)	4.5 Ω (typ.)
Q_G	7.7nC

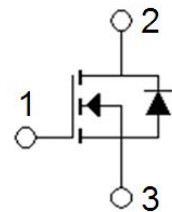


用途

- 高频开关电源
- 电子整流器
- LED 电源

APPLICATIONS

- High efficiency switch mode power supplies
- Electronic lamp ballasts based on half bridge
- LED power supplies



产品特性

- 低栅极电荷
- 低 C_{RSS} (典型值 10pF)
- 开关速度快
- 100%雪崩测试
- 高抗 dv/dt 能力
- RoHS 产品

FEATURES

- Low gate charge
- Low C_{RSS} (typical 10pF)
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- RoHS product

1 Gate
2 Drain
3 Source

订货信息 ORDER MESSAGE

订货型号 Order codes	印记 Marking	封装 Package	无卤素 Halogen Free	包装 Packaging	器件重量 Device Weight
HW2N65F	HW2N65F	TO-220FT	否 NO	条管 Tube	2.2g(typ)
HW2N65I	HW2N65I	IPAK	是 YES	条管 Tube	0.35g(typ)
HW2N65D	HW2N65D	DPAK	是 YES	条管 Tube	0.3g(typ)



广州华微电子有限公司

GUANGZHOU SINO-MICRO ELECTRONICS CO., Ltd.

绝对值 **ABSOLUTE RATINGS** ($T_C=25^{\circ}\text{C}$, unless otherwise specified)

项目 Parameter	符号 Symbol	数值 Ratings			单位 Unit
		IPAK	DPAK	TO-220FT	
漏极-源极击穿电压 Drain-Source Breakdown Voltage	V_{DSS}	650			V
漏极连续电流 Continuous Drain Current	I_D	$T_C=25^{\circ}\text{C}$ 2*			A
		$T_C=100^{\circ}\text{C}$ 1*			A
漏极脉冲电流 (注 1) Drain Current pulsed (note 1)	I_{DM}	8.4*			A
最高栅源电压 Gate-Source Voltage	V_{GSS}	± 30			V
单脉冲雪崩能量 (注 2) Single Pulsed Avalanche Energy (note 2)	E_{AS}	145			mJ
重复雪崩能量 (注 1) Repetitive Avalanche Current (note 1)	E_{AR}	20			mJ
二极管反向恢复最大电压变化速率 (注 3) Peak Diode Recovery dv/dt (note 3)	dv/dt	4.5			V/ns
耗散功率 Total Power Dissipation	P_D	$T_C=25^{\circ}\text{C}$ 88.7	85.5	16.7	W
		Derate above 25°C 0.71	0.68	0.13	W/ $^{\circ}\text{C}$
最高结温 Operating Junction Temperature	T_J	150			$^{\circ}\text{C}$
存储温度 Storage Temperature	T_{STG}	$-55 \sim +150$			$^{\circ}\text{C}$
焊接温度 Maximum Temperature for Soldering Leads	T_L	300			$^{\circ}\text{C}$

* 漏极电流由最高结温限制。

* Drain current is limited by junction temperature.

电特性 **ELECTRICAL CHARACTERISTICS** ($T_J=25^{\circ}\text{C}$, unless otherwise specified)

项目 Parameter	符号 Symbol	测试条件 Test conditions	最小值 Min	典型值 Typ	最大值 Max	单位 Unit
断态特性 Off Characteristics						
漏-源击穿电压 Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu\text{A}, V_{GS}=0\text{V}$	650	-	-	V
击穿电压温度特性 Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_J$	$I_D=250\mu\text{A}$, referenced to 25°C	-	0.81	-	V/ $^{\circ}\text{C}$
漏源极漏电流 Drain-Source leakage Current	I_{DSS}	$V_{DS}=650\text{V}, V_{GS}=0\text{V}$	-	-	1	μA
		$V_{DS}=520\text{V}, T_C=125^{\circ}\text{C}$	-	-	10	μA
栅源极漏电流 Gate-Source leakage current	正向 Forward	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=30\text{V}$		100	nA
	反向 Reverse		$V_{DS}=0\text{V}, V_{GS}=-30\text{V}$		-100	nA





项目 Parameter	符号 Symbol	测试条件 Test conditions	最小值 Min	典型值 Typ	最大值 Max	单位 Unit
通态特性 On Characteristics						
阈值电压 Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2	-	4	V
静态导通电阻 Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =1A	-	4.5	5.6	Ω
正向跨导 Forward Transconductance	g _{FS}	V _{DS} =40V, I _D =1A(note 4)	1	-	-	S
动态特性 Dynamic Characteristics						
输入电容 Input capacitance	C _{ISS}	V _{DS} =25V, V _{GS} =0V, f=1MHz	-	231	-	pF
输出电容 Output capacitance	C _{OSS}		-	28	-	
反向传输电容 Reverse transfer capacitance	C _{RSS}		-	10	-	
开关特性 Switching Characteristics						
导通延迟时间 Turn on delay time	t _{d(ON)}	V _{DD} =325V, I _D =2A, R _G =25Ω (note 4, 5)	-	9	-	ns
上升时间 Rise time	t _r		-	23	-	
关断延迟时间 Turn off delay time	t _{d(off)}		-	12	-	
下降时间 Fall time	t _f		-	22	-	
栅极电荷总量 Total Gate Charge	Q _G	V _{DS} =520V, I _D =2A, V _{GS} =10V (note 4, 5)	-	7.7	-	nC
栅-源电荷 Gate-Source charge	Q _{GS}		-	2.1	-	
栅-漏电荷 Gate-Drain charge	Q _{GD}		-	4.6	-	
源-漏二极管特性 Source-Drain Diode Characteristics						
正向连续电流 Continuous Forward Current	I _S		-	-	2	A
正向脉冲电流 Pulsed Forward Current	I _{SM}		-	-	8.4	A
正向压降 Forward Voltage	V _{SD}	V _{GS} =0V, I _S =2A	-	-	1.5	V
反向恢复时间 Reverse recovery time	t _{rr}	V _{GS} =0V, I _S =2A, dI _F /dt=100A/μs (note 4)	-	351	-	ns
反向恢复电荷 Reverse recovery charge	Q _{rr}		-	1.5	-	μC





热特性 THERMAL CHARACTERISTICS

项目 Parameter	符号 Symbol	最大值 Max			单位 Unit
		IPAK	DPAK	TO-220FT	
结到管壳的热阻 Thermal Resistance,Junction to Case	$R_{th(j-c)}$	1.41	1.46	7.4	$^{\circ}\text{C}/\text{W}$
结到环境的热阻 Thermal Resistance,Junction to Ambient	$R_{th(j-a)}$	90.6	86.6	50	$^{\circ}\text{C}/\text{W}$

注:

1:脉冲宽度由最高结温限制

2: $L=72.5\text{mH}$, $I_{AS}=2\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\Omega$, 起始结温 $T_J=25^{\circ}\text{C}$

3: $I_{SD}\leq 2\text{A}$, $di/dt \leq 100\text{A}/\mu\text{s}$, $V_{DD}\leq BV_{DSS}$, 起始结温 $T_J=25^{\circ}\text{C}$

4:脉冲测试:脉冲宽度 $\leq 300\mu\text{s}$, 占空比 $\leq 2\%$

5:基本与工作温度无关

Notes:

1:Pulse width limited by maximum junction temperature

2: $L=72.5\text{mH}$, $I_{AS}=2\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\Omega$, Starting $T_J=25^{\circ}\text{C}$

3: $I_{SD}\leq 2\text{A}$, $di/dt \leq 100\text{A}/\mu\text{s}$, $V_{DD}\leq BV_{DSS}$, Starting $T_J=25^{\circ}\text{C}$

4:Pulse Test:Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

5:Essentially independent of operating temperature





特征曲线 TYPICAL CHARACTERISTICS (curves)

Fig.1. On-state Characteristics

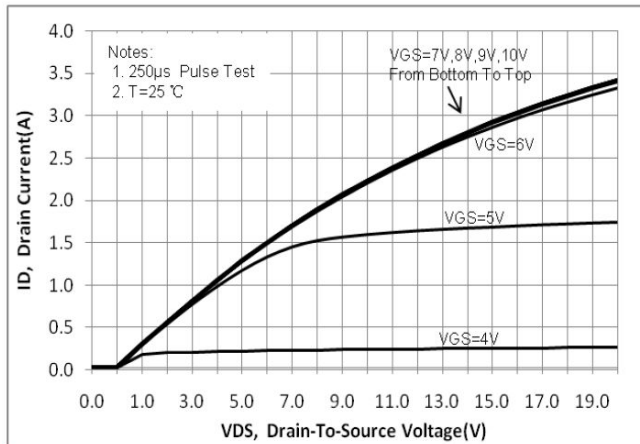


Fig.2. On-resistance variation vs. drain current and gate voltage

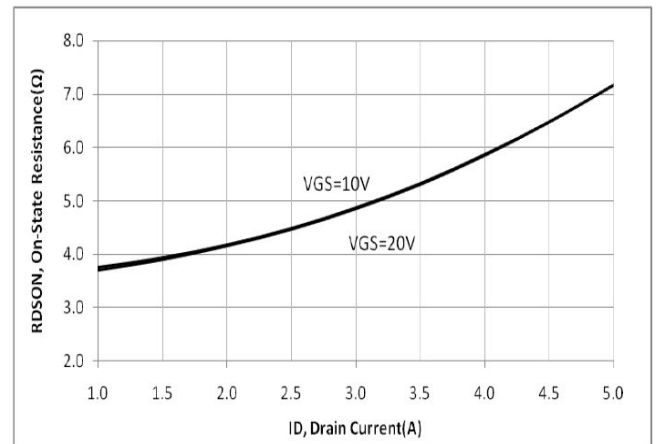


Fig.3. Gate charge characteristics

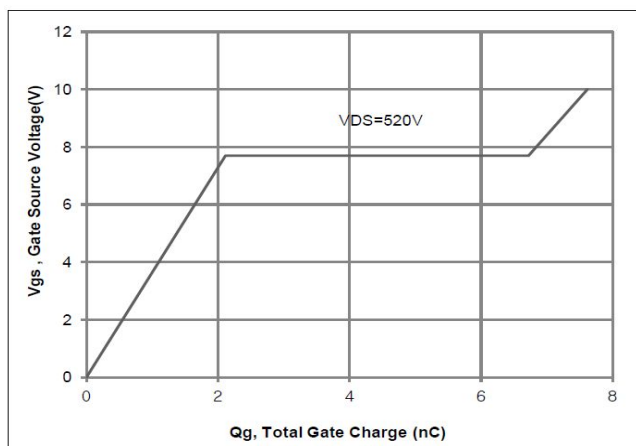


Fig.4. On state current vs. Diode forward voltage

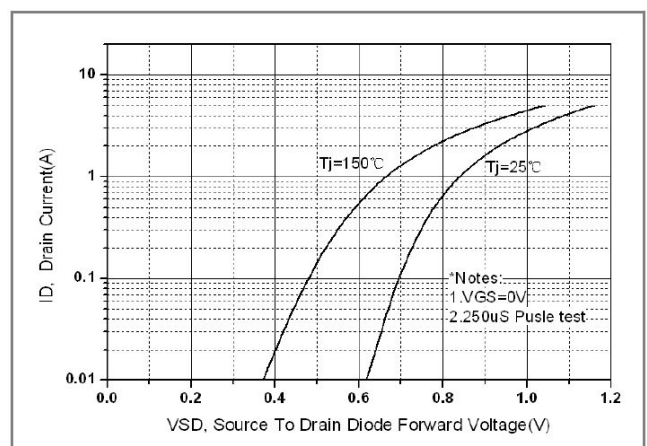


Fig.5. Breakdown voltage variation vs. Junction temperature

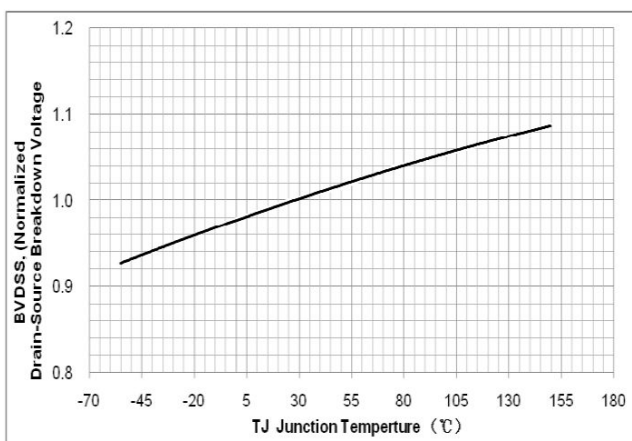


Fig.6. On resistance variation vs. Junction temperature

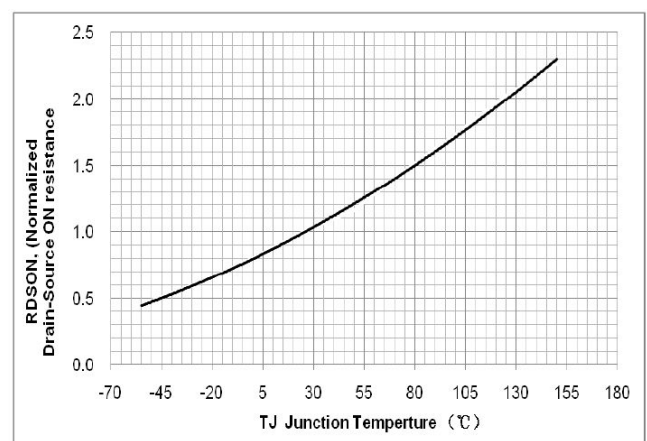




Fig.7. Maximum safe operating area(TO-220FT)

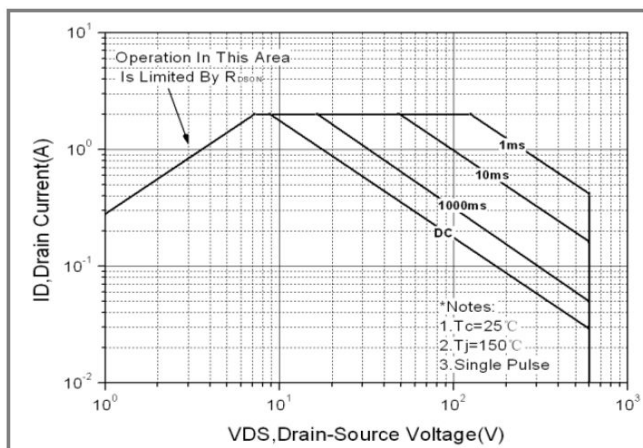


Fig.8. Transient thermal response curve(TO-220FT)

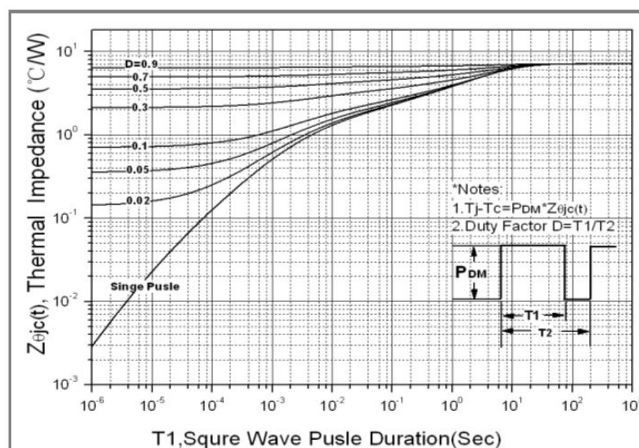


Fig.9. Maximum safe operating area(IPAK)

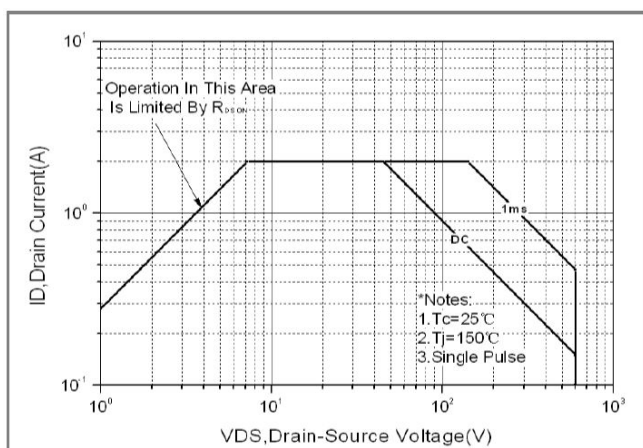


Fig.10. Transient thermal response curve(IPAK)

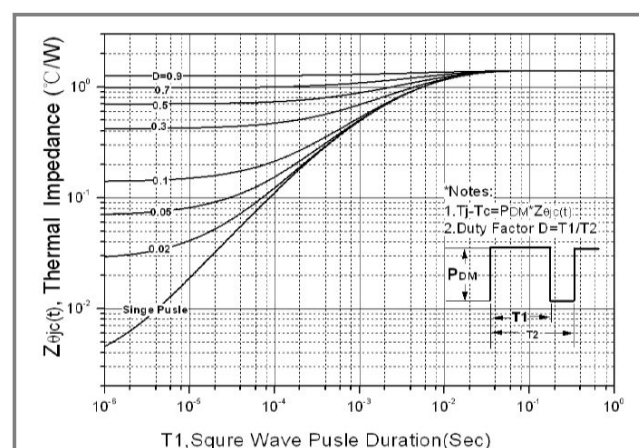


Fig.11. Maximum safe operating area(DPAK)

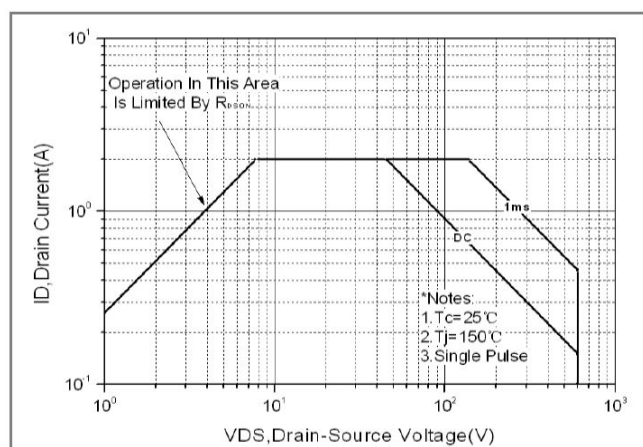


Fig.12. Transient thermal response curve(DPAK)

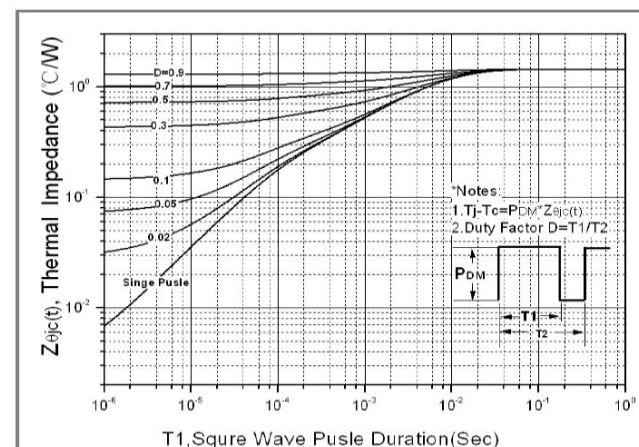




Fig.13. Capacitance characteristics

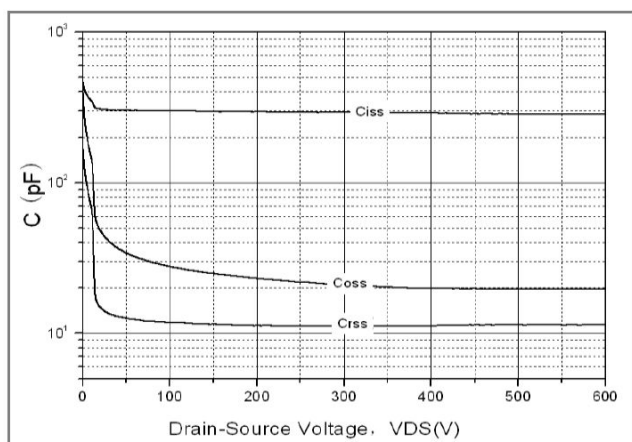


Fig.14. Gate charge test circuit & waveform

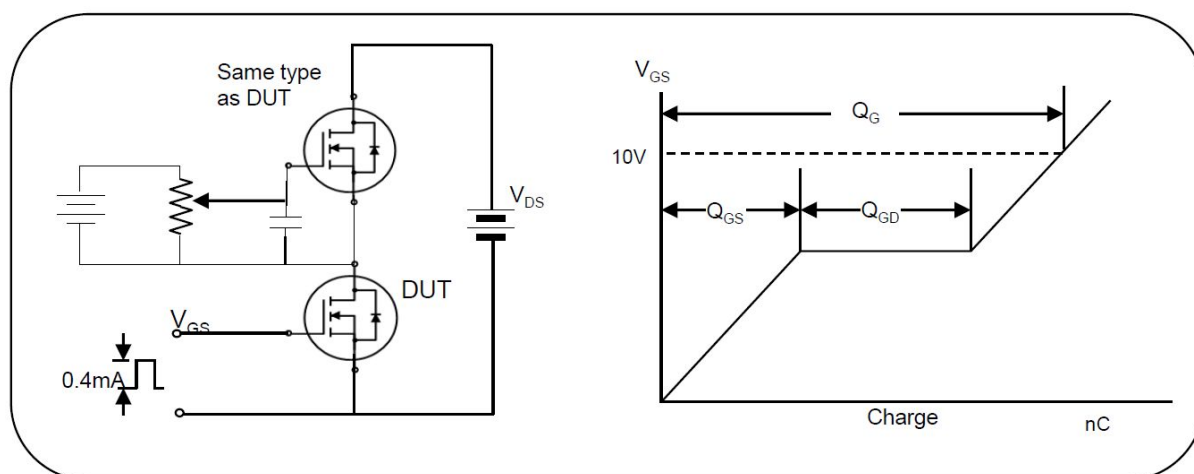


Fig.15. Switching time test circuit & waveform

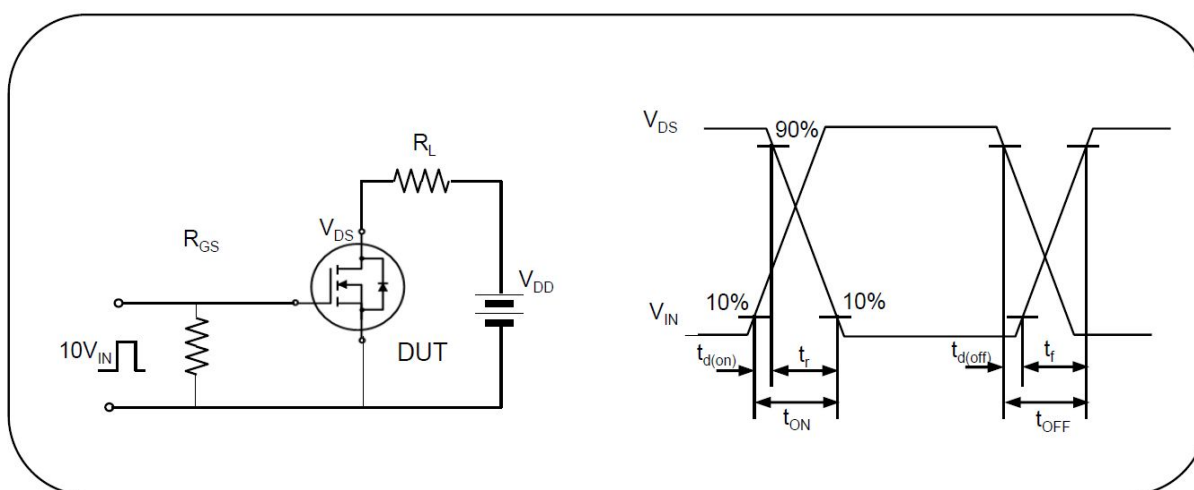


Fig.16. Unclamped inductive switching test circuit & waveform

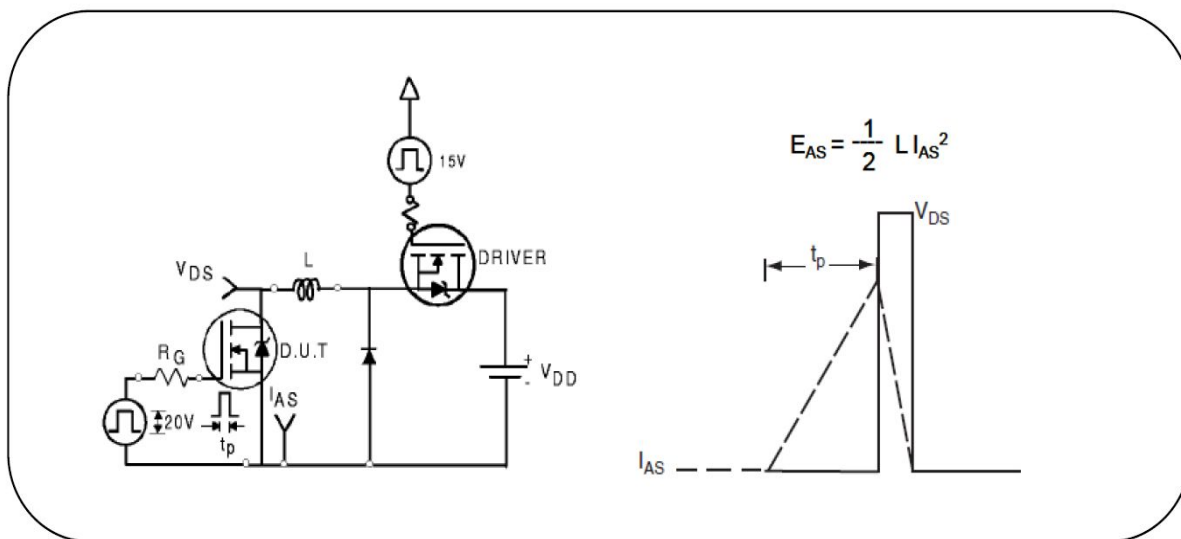
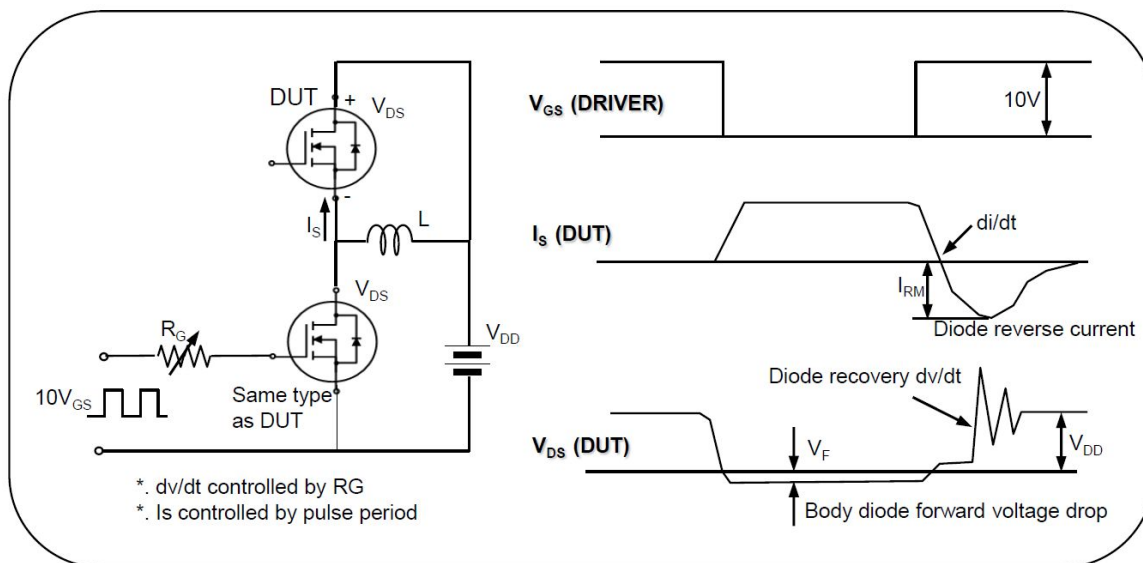


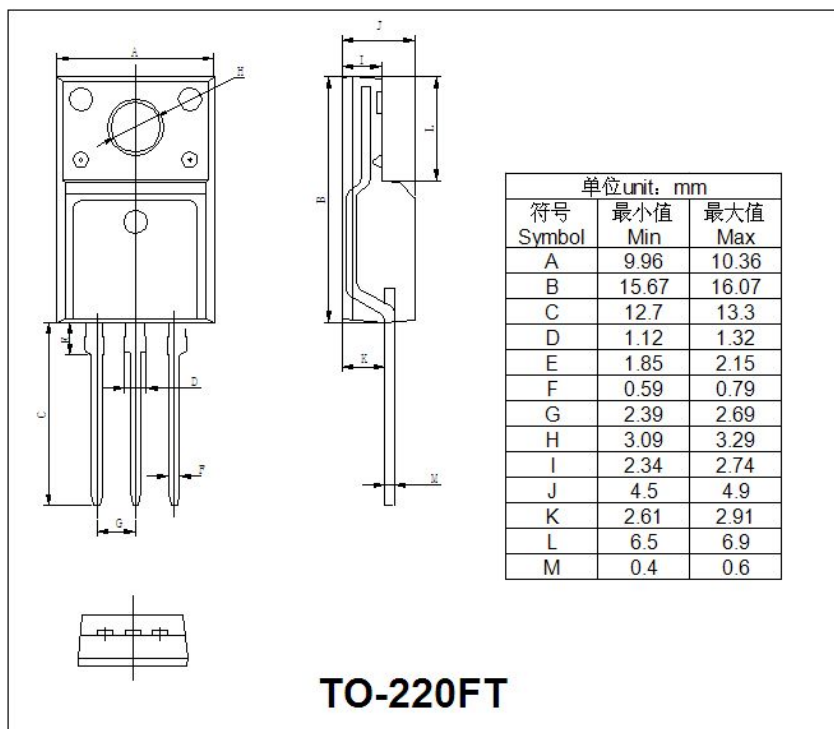
Fig.17. Peak diode recovery dv/dt test circuit & waveform



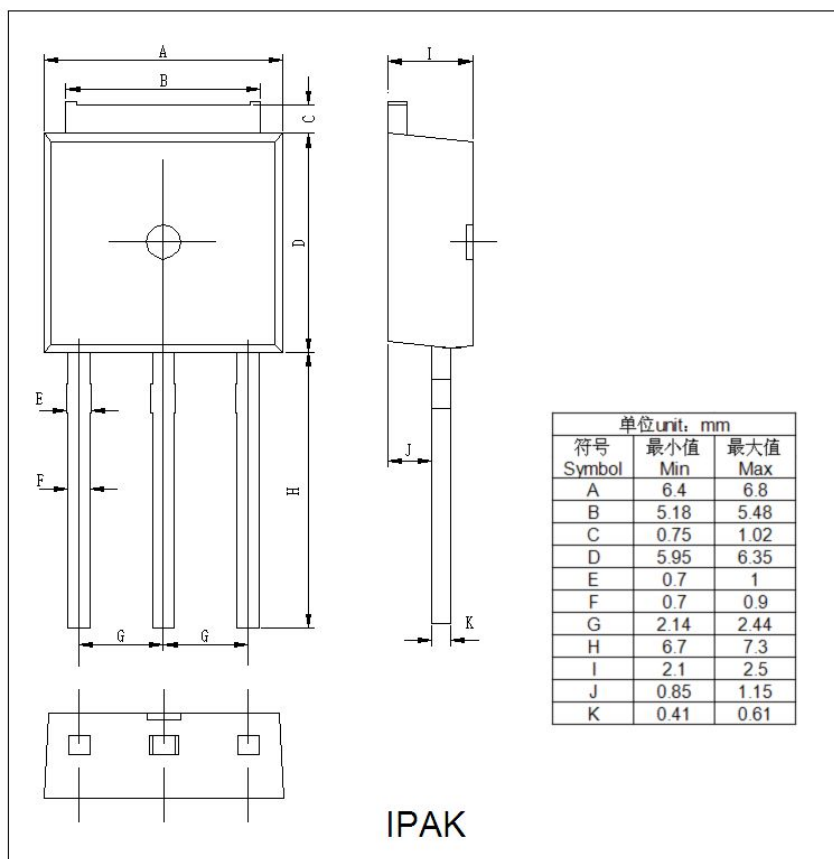


外形尺寸 PACKAGE MECHANICAL DATA

TO-220FT



IPAK





DPAK

